## Ph. D. Entrance Test - 2015 Subject: Computer Science and Engineering Paper - I

Important: Please consult your Admit Card/Roll No. slip before filling your Roll Number on the Test Booklet and Answer Sheet.

Roll No.	In Figure	In Words
O.M.R. Ansv	wer Sheet Serial No.	
Signature of Car	ndidute:	Signature of Invigilator:
Time: 60 Min		HE BOOK I ET UNTIL ASKED TO DO SO

## INSTRUCTIONS:

- Write your Roll No. on the Questions Booklet and also on the OMR Answer Sheet in the space provided and nowhere else.
- 2. Enter the Question Booklet Serial No. on the OMR Answer Sheet. Darken the corresponding bubbles with Black Ball Point/Black Gel Pen.
- Do not make any identification mark on the Answer Sheet or Question Booklet.
- 4. Please check that this Question Booklet contains \$0 Questions. In case of any discrepancy, inform the Assistant Superintendent within 10 minutes of the start of Test.
- 5. Each question has four alternative answer (A,B,C,D) of which only one is correct. For each question, darken only one bubble (A or B or C or D), whichever you think is the correct answer, on the Answer Sheet with Black Ball Point/Black Gel Pen. There shall be no negative marking for wrong answers,
- 6. If you do not want to answer a question, leave all the bubbles corresponding to that question blank in the Answer Booklet. No marks will be deducted in such cases.
- 7. Darken the bubbles in the OMR Answer Sheet according to the Serial No. of the question given in the Question Booklet.
- If you want to change an already marked answer, erase the shade in the darkened bubble completely.
- For rough work only the blank sheet at the end of the Question Booklet he used.
- 10. The Answer Sheet is designed for computer evaluation. Therefore, if you do not follow the instructions given on the Answer Sheet, it may make evaluation by the computer difficult. Any resultant loss to the candidate on the above account, i.e. not following the instructions completely, shall be of the candidate only.
- 11. After the test, hand over the Question Booklet and the Answer Sheet to the Assistant Superintendent on
- 12. In no case the Answer Sheet, the Question Booklet, or its part or any material copied/noted from this Booklet is to be taken out of the examination hall. Any candidate found doing so would be expelled from the examination.
- 13. A candidate who creates disturbance of any kind or changes his/her seat or is found in possession of any paper possibly of any assistant or found giving or receiving assistant or found using any other unfair means during the examination will be expelled from the examination by the Centre Superintendent/Observer whose decision shall be final.
- 14. Communication equipment such as mobile phones, pager, wireless set, scanner, camera or any electronic/digital gadget etc., is not permitted inside the examination half. Use of calculators is not
- 15. The candidates will not be allowed to leave the Examination Hall/Room before the expiry of the allotted time.

	If (0.34) <sub>8</sub> =(x) <sub>(0</sub> , then x is
-:	A) 0.3750
	B) 0.4375
	C) 0.4025
	D) 0.3650
	If the roots of the quadratic equation $x^2-12x+37=0$ are 5 and 8, then the base in which equation is written is  A) 11
	1007/30/
	B) 12
	C) 13
	D) 14
	The expression x'y'z'+x'yz'+xyz'-xyz' is equal to
	A) x'
	B) y <sup>1</sup> + z <sup>1</sup>
	C) z <sup>1</sup>
	D) x'+ y'
	The simplified form of $F(x, y, z) = \Sigma(2, 3, 6, 7)$ contains
	A) one variable
	B) two variables
	C) three variables
	D) no variable as the expression reduces to 1
5	Which of the following is true
	A) The characteristic equation of D flip-flop is $Q_{n+1} = D_n \oplus Q_n$
	B) The characteristic equation of T flip-flop is $Q_{n+1} = T_nQ_n + T_n'Q_n'$
	C) The characteristic equation of SR flip-flop is Q <sub>r=1</sub> = SQ <sub>r</sub> + R Q <sub>r</sub>
	asserted to the contest of an action of the three horses that the term of the
	A computer has 32-bit instructions and 12-bit addresses. If there are 250 two-
6	address instructions, how many one-address instructions can be formulated?
	address instructions, now many one-address made
30	A) 2 <sup>6</sup>
	B) 6*2 <sup>5</sup>
	C) 2 <sup>12</sup>
	D) 6+2 <sup>12</sup>
7	Which of the following statement is take:  A) CISC architecture makes use of variable-length instruction formats:  B) RISC architecture has single-cycle instruction execution:  C) CISC architecture is prosts relatively few addressing modes.
	The state of the s
8	A non-pipeline system takes 50 ns to process a task. The same task can be processed in a six-segment pipeline with a clock cycle of 10 ns. What is the speedup ratio of the pipeline for 100 tasks?
	A) 4.50
	B) 4.76
	C) 4.80
	(D) 4.84

y	A memory system contains a cache, a main memory, and a virtual memory. The access time of the cache is 5 ns and it has an 80 percent hit rate. The access time of the main memory is 100 ns, and it has a 99.5 percent hit rate. The access time of virtual memory is 10 ms. What is the average access time of the hierarchy?  A) 10024 ns B) 50099.5 ns C) 4000 ns
	D) 20099.5 ns
10	In C, which of the following operation can be applied on two pointer variables  A) addition  B) subtraction  C) multiplication  D) division
11	A binary search tree is generated by inserting in order the following elements: 49, 15, 62, 5, 20, 58, 98, 3, 8, 37, 60, 25, 100  The number of nodes in the left and right sub-tree of the root respectively arc  A) 7, 5  B) 5, 7  C) 8, 4  D) 4, 8
12	Worst case time complexity of Quicksort is  A) O(n log n)  B) O(log n)  C) O(n)  D) O(n <sup>2</sup> )
13	Data is stored in a circular array with N memory cells. If Rear < Front then number of elements in terms of rear and front are  A) N+Front-Rear-1  B) N+Front+Rear+1  C) N-Front-Rear+1  D) N-Front+Rear+1
14	Which of the following is generally considered the biggest open and unresolved question in theoretical computer science, where P and NP are complexity classes A) is P = NP?  B) is P ⊆ NP?  C) is NP ⊆ P?  D) is P + NP = φ
15	Consider T(n)=9T(n/3)+n A) T(n)=0(n <sup>3</sup> ) B) T(n)=θ(n <sup>2</sup> ) C) T(n)=O(n <sup>3</sup> ) D) T(n)=O(n)

16	The way a card game player arranges his cards as he picks them up one by one is
	an example of
	A) Merge sort
	B) Selection sort
	C) Insertion sort
	D) Bubble sort
17	Which of the following algorithm design technique is used in quick sort algorithm  A) Greedy Approach  B) Backtracking
	C) Dynamic Programming
	D) Divide and Conquer
18	DNS service is available at which of the following port?
	A) 51
	B) 53
	C) 57
	D) 59
19	Which of the following is responsible for retrieving mails from remote mail server
17	A) DNS
	B) SMTP
	C) POP3
	15.5 * 55.0 (50.0 ) :
000	D) HTTP
20	Which of the following represents a valid comment in XML.
	A) This is a comment
	B) This is a comment
	C) <#—This is a comment ->
	D) <@This is a comment>
21	<ul> </ul> tag is used to
	A) display the numbered list
	B) underline the text
	C) underline and hold the text
alari -	D) display the bulleted list
22	Cross compiler is a compiler capable of
	A) creating executable code for multiple platforms simultaneously
	B) creating executable code for a platform other than the one on which the
	compiler is running
	C) creating executable code that can be used for all platforms
	D) creating machine dependent as well as machine independent executable code
23	Lexical analysis is also called
	A) Scanning
	B) Parsing
	C) Syntax analysis
	D) Semantic analysis
24	Which of the following phase manipulates parse tree to verify symbol and typ
	information
	A) Intermediate Code Generation
	B) Optimization
	C) Semantic analysis
1	D) Syntax analysis

25	Which of the following is true
	A) I.R parsers are a type of bottom-up parsers
	B) LR parsers are a type of top-down parsers
	C) LR parsers are a type of middle-out parsers
	D) LR parsers can be of both types i.e. top-down as well as bottom-up
26	Which of the following is concerned with handling digital data
	A) ADM
	B) FDM
	C) WDM
327	D) TDM
27	If there are N routers from source to destination, which of the following closely represents total end to end delay in sending packet P (L→number of bits in the packet R→ transmission rate)
	A) (2*N*L)/R
	B) (N*L)/R
	C) (N*L)/2*R
	D) L/R
28	How many characters per seconds (7 bits + 1 parity) can be transmitted over a 2400
	bps line if the transfer is asynchronous (1 start and 1 stop bit)?
	A) 240
	B) 480
	C) 120
-	D) 300
29	Error detection at data link level is achieved by
	A) Bit stuffing
	B) Hamming Codes
	C) CRC
	D) Equalization
30	HTTPS service is usually available at which port
	A) 53
	B) 80
	C) 110
1500	D) 443
31	Which of the following situation happens if a non-recursive mutex is locked more
	than once?
	A) Deadlock
	B) Starvation
	C) Aging
	D) Abnormal Termination
32	At a particular time of computation the value of a counting semaphore is 7. Ther 20 P operations and 15 V operations were completed on this semaphore. The resulting value of the semaphore is
	A) 12
	B) 7
	C) 2
	D) 1
	ew.

33	Determine the number of page faults when references to pages occur in the following order: 1, 2, 4, 5, 2, 1, 2, 4. Assume that the main memory can accommodate 3 pages and the main memory already has the pages 1 and 2, with page 1 having been brought earlier than page 2. (Assume LRU algorithm is used)  A) 3  B) 4  C) 5  D) 6
34	Minimum average waiting time is given by which of the following scheduling algorithm  A) Priority B) FCFS C) Round-Robin D) SJF
35	In which of the following algorithm, the disk head moves from one end to the other, servicing requests along the way. When the head reaches the other end, it immediately returns to the beginning of the disk without servicing any requests on the return trip.  A) C-SCAN  B) SCAN  C) LOOK  D) C-LOOK
36	Which of the following relational algebraic operator can be used to display a subset of the tuples from a relation  A) Omega  B) Sigma  C) Pi  D) Lambda
37	Which of the following is minimal key for relation R (A, B, C, D) having following functional dependencies:  A→B, B→C, AC→D A) A B) B C) C D) D
38	Which of the following ensures the durability property of a transaction  A) Transaction Management  B) Application Programmer  C) Recovery Management  D) Concurrency Control
39	Which of the following rule is not true for B*-tree:  A) If the root is not a leaf node, it must have at least two children  B) Leaf nodes are linked in order of key values  C) The tree must always be balanced  D) The number of key values contained in a non-leaf node are always more than the number of pointers

40	What is the RDBMS terminology for the number of attributes in a relation?  A) multiplicity  B) domain  C) cardinality
41	D) degree In Moore machine, the output depends on A) present state only B) present input only C) present state and present input D) neither present state nor input
42	Which of the following statement is not true  A) Every language that is defined by regular expression can also be defined by finite automata  B) Context-sensitive grammars are type-1 grammars  C) Linear bounded automation recognizes type-0 grammar  D) For every nondeterministic finite automaton, a deterministic finite automaton can be found
43	Which of the following type of production is not allowed in Greibach Normal Form:  A) A→aB  B) A→BC  C) A→aBC  D) A→a
44	A PDA can recognize A) context-free language B) context-sensitive language C) type 0 language D) any language
45	A TM can't solve halting problem is  A) true  B) false  C) still an open question  D) tautology
46	A program P calls two subprograms P1 and P2. P1 can fail 50% times and P2 can fail 40% times. The program P can fail:  A) 50 %  B) 70%  C) 60 %  D) 45%
47	Configuration Management is not concerned with  A) managing changes to the source code  B) handling documentation changes  C) choice of hardware configuration for an application  D) maintaining versions of software

48	Which of the following is normally considered as an adjunct to the coding step  A) regression testing  B) system testing  C) integration testing  D) unit testing
49	Cohesion is a qualitative indication of the degree to which a module  A) can be written more compactly  B) focuses on just one thing  C) is connected to other modules  D) is able to complete its function in a timely manner
50	Basis Path Testing is  A) White Box testing technique  B) Black Box testing technique  C) Grey Box testing technique  D) Integration testing approach

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